

# SQA-8448 JUNCTION TRAFFIC SIGNAL DESIGN SHEET PART A

Site Ref:	06/000042	Revision No:	2	Date:	22/11/2021
Address:	ROCHESTER WAY - WELLING WAY				
SI Engineer:		WBS Code:	SC.3039.706.042		
Drawing Number:	PRO/06/000042/03	Primary Mode:	FT UTC		
UID:	113733	Fallback Mode:	VA		

Parallel Stage Stream Site Refs (if Applicable)

Reason For Changes/Update

Full modernisation of junction

## EXISTING LIVE DESIGN INFORMATION

Item	Source	Version	File Location (Hyperlink)
Existing Staging	Controller Spec	7	SFM
Existing Phases	Controller Spec	7	SFM
Existing Stage Moves	Controller Spec	7	SFM
Existing Intergreens	Controller Spec	7	SFM
Existing Phase Delays	Controller Spec	7	SFM
Existing CLF/Timetabl	Controller Spec	7	SFM
UTC Pattern	Controller Spec	7	SFM

Is there any live design information which cannot be indicated above?

## PROPOSED STAGING

Stream/Stage

0	1	2	3
DUMMY ALL RED F	A B	B C	D
/	/	/	/
/	/	/	/

Are there any changes to stages?

## PART A

Date: 22/11/2021

Address: ROCHESTER WAY - WELLING WAY

## PROPOSED PHASE AND INTERGREEN TIMINGS

[illegible]

Yes
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Intergreens squared off so no early starts or cut offs occur

Yes

Please detail which intergreens are variable in the *Additional Notes* section

(SET 1: UTC, MANUAL AND CLF MODES)

Yes

(SET 1: UTC, VA, MANUAL FT, CLF MODES)

(SET 2:, FT, PRIORITY, MOVA & HURRY CALL)

DEMAND  
DEPENDANCY[illegible]

Any additional Proposed Prohibited/VIA move sets should be detailed in the Additional Notes section

Yes

(Not on OTU - do not include in UTC Word)

PROPOSED UTC LINK  
(On OTU - include in UTC Word)

## PROPOSED UTC MODE DEPENDANCY (STREAMS)

Linked Site

Linked Site

	0	1	2	3	4
0	0	1	0	1	0
1	1	0	1	0	1
2	0	1	0	1	0

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## PROPOSED PHASE DELAYS

No.	Phase	From	To	Secs	No.	Phase	From	To	Secs	No.	Phase	From	To	Secs	No.	Phase	From	To	Secs
0	NO PHASE DELAYS				24					48					72				
1					25					49					73				
2					26					50					74				
3					27					51					75				
4					28					52					76				
5					29					53					77				
6					30					54					78				
7					31					55					79				
8					32					56					80				
9					33					57					81				
10					34					58					82				
11					35					59					83				
12					36					60					84				
13					37					61					85				
14					38					62					86				
15					39					63					87				
16					40					64					88				
17					41					65					89				
18					42					66					90				
19					43					67					91				
20					44					68					92				
21					45					69					93				
22					46					70					94				
23					47					71					95				

Are there any changes to phase delays ☒ Yes

Link No	Link to Ped Controller at	Associated Stage or Phase	Start or End	Link Delay Time	Inhibit Release Period	Override Time
0						
1						
2						
3						

*Fixed Time to Current Max to be Used as Last Mode in Priority, Unless Otherwise Stated Below*

### Additional Notes:

Traffic intergreens variable due to Speed assessment in operation at this site. Stage 2 dependant on phase C being called by the CP loop.

	Role:	Name:	Date:
(ENG) S1 Engineer:	Engineer		22/11/2021
(ENG) S2 Engineer:	TfL Assurance Engineer		29/11/2021

## PART 1

Date: 22/11/2021

## NPD To Check Controller Spec?

## PART I

## NPD To Check Controller Spec?

## PROPOSED CLF PLANS

[illegible][illegible]

## PROPOSED TIMETABLE

Yes

	Day Type	Hours	Mins	Secs	Plan No.
0	7	6	0	0	MAXA
1	7	8	0	0	MAXB
2	7	10	0	0	MAXA
3	7	12	0	0	MAXC
4	7	15	0	0	MAXD
5	7	20	0	0	MAXA
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					
16					
17					
18					
19					
20					
21					
22					
23					
24					

## PROPOSED VA MAX SETS

☒ Yes

		MAX SET							
		A	B	C	D	E	F	G	H
PHASE	A	23	48	31	52				
	B	23	48	31	52				
	C	7	7	12	7				
	D	16	24	16	20				
	E								
	F								
	G								
	H								
	I								
	J								
	K								
	L								
	M								
	N								
	O								
	P								
	Q								
	R								
	S								
	T								
U									
V									
W									
X									
Y									

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## PROPOSED INTERSTAGE TABLE

	TO															
FROM	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0		2	2	2												
1	3		7	7												
2	x	x		7												
3	3	6	6													
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																

## iBUS LOCAL EXTENSION TIMERS

Is EP/EC configured?

No

Are there any interstage changes?

Yes

## PROPOSED UTC CONTROL / REPLY WORD

Please mark all demand dependent stages with a #

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
CONTROL	IF1	#IF2	#IF3	IDX			ITS									
REPLY	IG1	#IG2	#IG3	IJD	IJL	ICF	IRT									
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
REPLY																

RTC SYNC  
TIME

04:31

Are there any changes to the UTC control/reply word?

Yes

## NETWORK PERFORMANCE DEPARTMENT CONSIDERATIONS

What Modelling Has Been Produced?

(Please include Hyperlink to modelling location)

Additional Information:-

	Role:	Name:	Date:
(NPD) Checker:	NM		01/12/2021
(NPD) Approver:	PNM		02/12/2021